

# ELIMINATION OF HARMONICS AND SHORT CIRCUIT BY USING DVR WITH REPETITIVE CONTROL

**Kusumalatha Vallepalli<sup>1</sup>, G.Naveen<sup>2</sup>**

<sup>1</sup> M.Tech Scholar (PS), Nalanda Institute of Engineering and Tech. (NIET),  
Siddharth Nagar, Guntur, A.P. (India)

<sup>2</sup> Asst.Professor (EEE), Nalanda Institute of Engineering and Technology(NIET),  
Siddharth Nagar, Guntur, A.P. (India)

## ABSTRACT

*This paper implemented the dynamic voltage restorer with the repetitive controller system arrangement to mitigate the important power quality issues such as voltage sags, distortions, unbalanced voltages and single and three phase faults. In this proposed system consisting of one feed forward parameter to control and maintained the zero error in the steady state and it improves the transient response of the system. In this paper additionally controls the grounding faults total harmonic distortions and harmonics and line-line faults, line-ground faults in the power system are compensated by the repetitive controller in the DVR. The implemented models are designed in the MATLAB/ SIMULINK is discussed. The usage of repetitive controller the problems in the system is compensated then correspondingly the performance is enhanced.*

***Index Terms—Dynamic Voltage Restorer (DVR), Power Quality (PQ), Total Harmonic Distortion (THD), Voltage Sag, Repetitive Controller***

## I. INTRODUCTION

The significance of power quality problems (PQ) has risen very noticeably over the most recent two decades outstanding to a discernible enlarge in the quantity of apparatus which is perceptive to adverse PQ surroundings, the instability developed by non linear and disturbance loads, and the explosion of non renewable energy sources, amongst others. At smallest amount 50% of all PQ turbulence are of the voltage excellence type, where the attention is the learning of any divergence of the voltage characteristics from its natural conditions. The most excellent well-known distortions are voltage swells and sag faults, distortions and lay to rest un balanced load voltages, and, for three-phase fault systems, voltage fluctuations. Voltage sag is in general affected by short-circuiting problems in the power system network or by the preliminary up of induction motors of higher in rating. The resulting adverse penalties are a lessening in the energy distributed of electric motors and the disengagement of susceptible equipment and manufacturing procedures brought to a declined. A complete explanation of voltage sag can be established. Distortions are bent by nonlinear or imbalanced loads apparatus, such as electric arc lambs variable energy drives, huge expenditures of arc ejection lamps, and loads which employ power quality in electronics. Rippled content currents produced by a disturbance device or formed as a consequence of obtainable harmonic problems will aggravate copper and iron problems in electrical continents. In rotating equipment, they determination construct pulsating torques and induction heating. Voltage fluctuations are generally produce about by disturbed loads or unbalanced short circuit problems, thus

generating heat losses in synchronous speed machines and, in some tremendous cases, foremost to shut downs and apparatus failure. The DVR is fundamentally a voltage source converter (VSI) coupled in progression with the AC arrangement via an interconnecting transfer voltage i.e. transformer, which was initially conceived to get better voltage sags troubles. The basic in commission standard following the Dynamic Voltage Restorer is inject the additional voltage of an in phase series connected voltage with the inward supply to the consumer load, sufficient enough to bring back the voltage to its pre sag situation. Its rate of accomplishment in struggle of voltage sags in definite installations is well renowned, this being one of the possibilities why it functioned to attract a immense deal of attention in manufacturing and in academic implementations. Investigation work has been expressed on DVR two-level model and multilevel technologies well as on manage and procedure. The concluding may be separated into quite a few topics.

1) The relationship, whether two-level or higher level, reproduce to the accessibility, or other control technology, of energy storage arrangement, the output filter selected, and the capability to abandon out unbalanced voltages in 3-phase four-wire techniques.

2) The voltage-sag gratitude several procedures have been selected to perceive the immediate of sag manifestation, such as dimension of the crest value of the grid side produced voltage. A complete assessment of these can be originated.

3) The direct control strategy. The DVR might be functioned to transport in the series voltage recording to various different criteria, such as lowest energy substitute with the grid. The three the majority accepted strategies to recompense voltage sags are explained.

a) Presag mitigation technique. The additional extra injected DVR voltage is deliberate to basically recompense the required load voltage to its presag circumstance; b) In phase compensation technique. The DVR produced voltage is constantly in segment with the grid connected voltage; c) most favorable energy mitigation. This control strategy reduces the power transfer connecting the power storage arrangement and the grid throughout steady-state operation process. Even though these are the most excellent distinguished control techniques, many strategies are individual made to expand new ones to facilitate better DVR consumption as discussed.

4) The design of the supervise law. The compensator is frequently deliberated with some specific aims definitely in mind, like as the type of disturbances it must to reorganize, the rapidity of time result, error in steady-state situation, etc. The majority of the published work on DVR uses a simple proportional-integral (PI) control law implemented in a frame of reference which rotates with the frequency of the grid side voltage. The fundamental come close to its adequate to enable voltage sag damages, to necessitate zero tracking error for the elementary minimum constituent, and to mitigate certain levels of unbalanced conditions. Nevertheless, this straightforward manage law is inadequate when commerce with high-quality application features and more multifaceted modules are mandatory. The previous orientation adds resonant supervise filters to the accessible PI supervise process in order to eradicate the distortions voltages. The main complicatedness of this submission is that one filter is obligatory for each operation to be rewarded if the association is concerned, and only half that magnitude if the association is proscribed. The latter direction takes the equivalent technique of accumulation a feed forward ring to the criticism PI controller in order to get better the regulation overall presentation, captivating into explanation the time delay procedure of the operated system and the DVR production filter contents. This work concentrated on the propose of a closed-loop modulation law for a 2-level DVR, depended on the supposed repetitive control progress, targetting that at mitigating key voltage-quality turbulence, specifically, voltage sags, distortions voltages, and voltage fluctuations. Repetitive control strategy was first

implemented in to compensate periodic turbulence and to identify periodic position signals with zero error. The repetitive control deal was originally applied to abolish speed oscillations in electric motors except it has because been proposed in a extensive range of advanced power-electronics application features. A comprehensive examination of different repetitive control strategy arrangements is calculated in a repetitive regulator is employed to attain an production voltage with low deformation in a constant voltage, constant regularity 3-phase PWM inverter. In a repetitive modulator is implemented to accomplish the zero tracking mistake in the output current waveform of a three phase rectifier in order to progress its higher power factor. The repetitive controller accessible in this occupation has a wider range of requirements; it is necessary in a DVR organization to restructure problems such as voltage sags and swells, distortion voltages, and voltage fluctuations within a specified bandwidth. Unlike other control technologies, which in addition have a equivalent range of accessibility, only one compensator is activated to achieve the neutralize all three turbulence concurrently. The manage arrangement consists grid voltage feed forward term to get better the arrangement transient reactions, and a closed-loop control arrangement which uses a feedback of the load electrical energy with the recurring organizer in order to necessitate zero identifying error in steady state operation.

## II. MODEL OF THE DVR –CONNECTION SYSTEM

A characteristic test organization, corresponding to a DVR, is demonstrated in Fig.1. Different types of loads are coupled at the point of common coupling (PCC), together with linear load operations and nonlinear load operations, and sensitive load operations. The series association of the voltage-source modulator (VSC) construction up the DVR through the ac arrangement is effected by resources of a coupling transformer their primary is associated in series flanked by the mains and the consignment. Even though a unreceptive LC filter is in general used to obtain a switching-ripple-free DVR voltage, in this paper, this filter is not measured in order to completely assess the harmonic mitigating property of the recurring organizer.

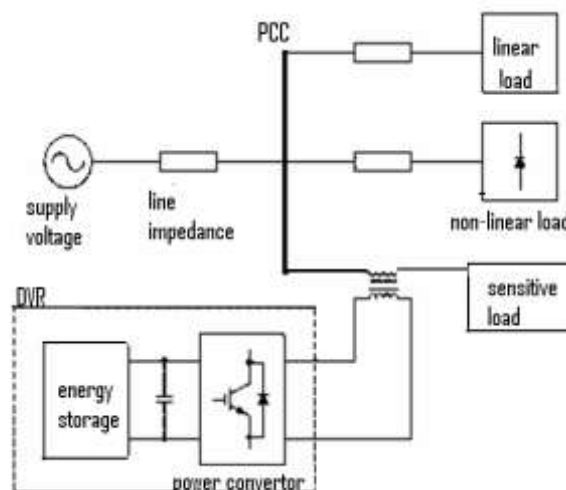


Fig.1: System Configuration Across Sensitive Load

## III. DESIGN OF THE CONTROL SYSTEM

The proposal of the control system is to standardize the load voltage in the occurrence of various types of instability. The control arrangement projected in this paper is depended on the employ of a feed forward term of

the voltage at the PCC to acquire fast transient reactions, and a comment expression of the produced load voltage to ensure zero error in stable state. The freight voltage is the generated by the adjusting of the controller delays are  $t_0$  is not faithfully known and the closed loop classification will not be generate the stable out. To find out this problem, a customized compensator  $C(S)$  is implemented and finding the transfer function in order to generate the time delay of the required output. The output is manipulated by the gains of closed loop control system are utilized.

#### IV. CIRCUIT DESCRIPTION

The power system network and the control organization have been designed in MATLAB/ simulink .The experiment scheme is utilized of a 400 V 50 Hz foundation which produces three dissimilar loads: it requires a squirrel cage induction machine, A non-linear performed load which consists of an hysterical 3-phase rectifier with an inductive-resistive depended load and a 3- phase sensitive load is proposed. Which performed of a star complete up of a confrontation associated in succession with an inductance in every phase? A two level DVR is coupled linking between the PCC and the sensitive load by earnings of a 20-KVA combination transformer with a unity turns ratio and a star connected secondary winding of the designed circuit.

**Repetitive controller:** it is purely depended on the internal generation of the periodic wave forms like as tracking reference signal or else the rejecting the periodic signal reference signal based implementations are developed. It is used to operate the controller for the protecting the device when ever various faults are happened in the power system like as sags, swells, harmonics those are compensated it performed like as a multi functional device.

**Faults:** Faults are presented in the power system those are sudden abnormal faults. Short circuit is presented whenever the current is bypassed the normal load condition then these faults are occurred in the power system. Open circuit faults are generated whenever the circuits are interrupted by some failures these are produced. In the three phase analysis the faults are developed in the line-line faults or line-ground or earth faults are produced. In the symmetrical and asymmetrical faults are disturbed the power quality in the power systems. These symmetrical faults are generated in the three phases in these three phase are effected in equal manner. In the asymmetrical faults are unbalanced faults those affected by the ionization of electronics that means the lines are closer to the physical contact in case of line-line and the short circuit is produced in between the line-ground with the physical body rarely affected whenever the storms are come into the double lines with respect to the ground is caused the double line with ground fault. In the non linear load conditions are presented in the power systems then the load is disturbed due to the presence of non linear load. These loads are does not obeys the ohms law operations these are like as diodes op-amps are key role.

#### V. CONCLUSION

The DVR with the an advanced repetitive controller accessible in this thesis is able to progress the Power Quality troubles like the voltage sags, distortion voltages and voltage fluctuations for various types of faults .The results prearranged that irregular of the amplitude and period of the fault accessible in the DVR is capable to improve the voltage sags, harmonic voltages and voltage fluctuations across the sensitive load. An extraordinary aspect of this control system is its effortlessness; only one regulator does three jobs i.e., one manager neutralizes the three power quality instability problems. This repetitive compensator in this argument has a prompt transient reaction and maintains zero error in the steady state error for any sinusoidal required

reference contribution and for any sinusoidal troubles whose frequencies are a numeral several of the minimum fundamental switching frequency. To accomplish this, the compensator has been implemented with a feed advance expression and criticism term. Simulation outcome results are generate using MATLAB/SIMULINK demonstrate that the repetitive regulator and the DVR have proposed good consequences in decreasing the Power Quality faults across the sensitive load variations.

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## AUTHOR DETAILS



**KUSUMA LATHA VALLEPALLI**, Pursuing M.tech (PS) from Nalanda institute of Engineering and Technology(NIET), *Siddharth Nagar, Kantepudi village, satenepalli Mandal Guntur Dist.,A.P, INDIA*



**G.NAVEEN**, working as Asst.professor (EEE) from Nalanda institute of Engineering and Technology (NIET), *Siddharth Nagar, Kantepudi village, satenepalli Mandal Guntur Dist.,A.P, INDIA*